

The PNP/5280 MII Signals

The PNP/5280 PGA-169 pinout offers a MII (Media Independent Interface) interface for external PHY devices.

Pin	Name	Function
J2	ECOL	MII Collision Detect
J1	ECRS	MII Carrier Sense
H1	EMDC	MII Management Data Clock
K1	EMDIO	MII Management Data I/O
G2	ERXCLK	MII Receive Clock
F1	ERXD0	MII Receive Data Bit 0
F2	ERXD1	MII Receive Data Bit 1
E1	ERXD2	MII Receive Data Bit 2
E2	ERXD3	MII Receive Data Bit 3
G1	ERXDV	MII Receive Data Valid
H2	ERXER	MII Receive Error
L2	ETXCLK	MII Transmit Clock
M1	ETXD0	MII Transmit Data Bit 0
M2	ETXD1	MII Transmit Data Bit 1
N1	ETXD2	MII Transmit Data Bit 2
N2	ETXD3	MII Transmit Data Bit 3
L1	ETXEN	MII Transmit Enable
K2	ETXER	MII Transmit Error

Table 1: PNP/5280 MII Signals