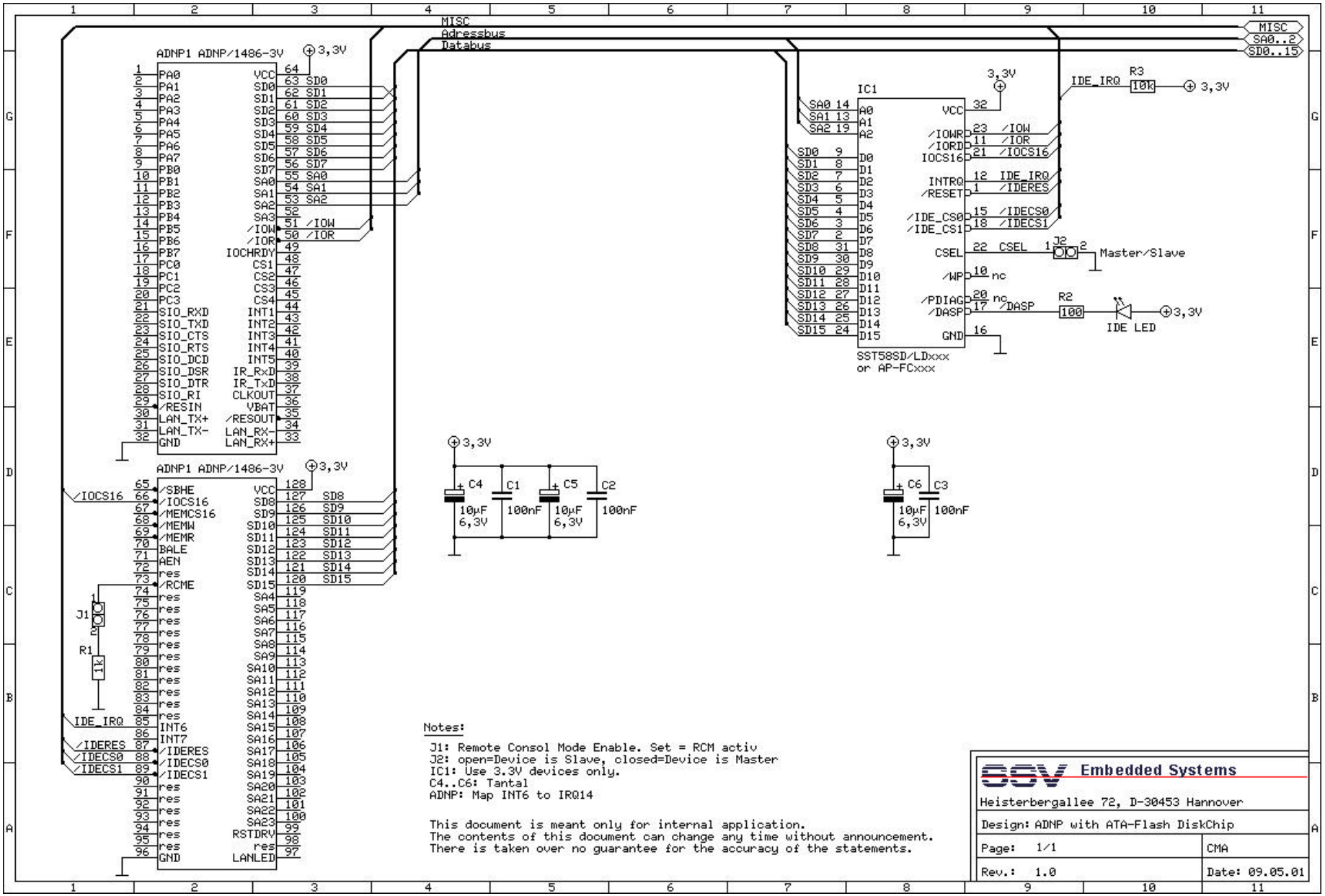
MISC
Adressbus
Databus
ADNP1 ADNP/1486-3V
3,3V
IC1
SST58SD/LDxxx
or AP-FCxxx
IDE_IRQ
R3
10k
Master/Slave
IDE LED
R2
100
C4
C1
C5
C2
C6
C3
10µF
6,3V
100nF
J1
R1
1k
J2
Notes:
J1: Remote Consol Mode Enable. Set = RCM activ
J2: open=Device is Slave, closed=Device is Master
IC1: Use 3.3V devices only.
C4..C6: Tantal
ADNP: Map INT6 to IRQ14
This document is meant only for internal application.
The contents of this document can change any time without announcement.
There is taken over no guarantee for the accuracy of the statements.
Embedded Systems
Heisterbergallee 72, D-30453 Hannover
Design: ADNP with ATA-Flash DiskChip
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CMA
Rev.: 1.0
Date: 09.05.01